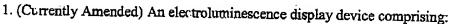
AMENDMENT TO CLAIMS:



a display pixel region disposed on a substrate and having an electroluminescence element including an emissive layer between first and second electrodes; and

a drive circuit region having a peripheral drive circuit that is integrated on said substrate, disposed on the same substrate and said drive circuit region having thin film transistors for driving said electroluminescence element; wherein

said first electrode entirely overlaps said display pixel region and is absent from at least said drive circuit region, said first electrode is a common cathode, and said second electrode is a discrete anode.

- 2. (Original) The device defined in Claim 1 wherein said first electrode is formed as a common electrode in said display pixel region.
 - 3. (Original) The device defined in Claim 1 wherein:

said cisplay pixel region includes first and second thin film transistors for driving said electroluminescence element;

an insulating film is formed overlapping said first and second thin film transistors and said thin film transistors of said drive circuit region; and

said first electrode is formed over said insulating film in a position opposing said display pixel region.

- 4. (Original) The device defined in Claim 3 wherein a circuit in said drive circuit region includes a CMOS connection structure in which a p-type channel thin film transistor and a n-type channel thin film transistor are complementarily connected.
 - 5. (Original) The device defined in Claim 1 wherein:

said thin film transistors of said drive circuit region are bottom gate type transistors having gate electrodes located beneath an active layer; and

said first electrode is formed over an insulating layer extending on the entire substrate on an opposite side of said active layer from which said gate electrodes are located, said first electrode formed overlapping said display pixel region.

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- 7 (Previously Amended) The device defined in Claim 1 wherein said emissive layer is a layer including an organic compound as an emissive material.
- 8. (Currently Amended) An electroluminescence display device comprising a substrate provided with:
- a display pixel region having an electroluminescence element including an emissive layer between an anode and a cathode, and first and second thin film transistors for driving said electroluminescence element; and
- a drive circuit region having a peripheral drive circuit that is integrated on said substrate, disposed surremaining said display pixel region and said drive circuit region having third thin film transistors for driving said first and second thin film transistors; wherein

said cathode is disposed in said display pixel region and is absent from said drive circuit region.

- 9. (Original) The device defined in Claim 8 wherein said cathode on said substrate is formed over the entire display pixel region as a common electrode, and is absent from at least said drive circuit region.
- 10. ((riginal) The device defined in Claim 8 wherein a circuit in said drive circuit region includes a CMOS connection structure in which a p-type channel thin film transistor and a n-type channel thin film transistor are complementarily connected.
 - 11. (Original) The device defined in Claim 8 wherein:

said third thin film transistors of said drive circuit region are bottom gate type transistors having gate electrodes located beneath an active layer; and

said cathode is formed over an insulating layer extending on the entire substrate on an opposite side of said active layer from which said gate electrodes are located, said cathode formed overlapping said display pixel region.

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12. (Currently Amended) An emissive display device comprising:

a display pixel region disposed on a substrate and having an emissive element including an emissive layer between first and second electrodes; and

a drive circuit region having a peripheral drive circuit that is integrated on said substratedisp seed on the same substrate surrounding said display pixel region, said drive circuit region having thin film transistors for driving said emissive element;

wherein

said first electrode overlaps the entire display pixel region and is absent from at least said drive circuit region, said first electrode is a common cathode, and said second electrode is a discrete anode.

13. (Currently Amended) An electroluminescence display device comprising a substrate provided with:

a display pixel region having an electroluminescence element including an emissive layer between an anode and a cathode, and first and second thin film transistors for driving said electroluminescence element, said cathode is formed in a layer extending above a layer in which said anode is formed; and

a drive circuit region having a reripheral drive circuit that is integrated on said substrate, disposed surrounding said display pixel region and said drive circuit region having third thin film transistors for driving said first and second thin film transistors; wherein

said cathode is disposed in said display pixel region and is absent from said drive circuit region.

- 14. (New) The device as defined in Claim 1, wherein said cathode includes an end portion that extends to an area between said display pixel region and said drive circuit region.
- 15. (New) The device as defined in Claim 8, wherein said cathode includes an end portion that extends to an area between said display pixel region and said drive circuit region.
- 16. (New) The device as defined in Claim 12, wherein said cathode includes an end portion that extends to an area between said display pixel region and said drive circuit region.
- 17. (New) The device as defined in Claim 13, wherein said cathode includes an end portion that extends to an area between said display pixel region and said drive circuit region.

18. (New) An electroluminescence display device comprising:

a display pixel region disposed on a substrate and having an electroluminescence element including an e nissive layer between first and second electrodes; and

a drive circuit region having vertical and horizontal drive circuits that are located in a peripheral drive circuit region formed surrounding said display pixel region and are integrated on said substrate, said drive circuit region baving thin film transistors for driving said electrolumine-cence element; wherein

said first electrode entirely overlaps said display pixel region and is absent from at least said drive circuit region,

said first electrode is a common cathode, comprises an opaque metal material, and constitutes an uppermost layer of said electroluminescence element, and

said second electrode is a discrete anode.

- 19. (New) The device as defined in Claim 18, wherein said cathode includes an end portion that extends to an area between said display pixel region and said drive circuit region.
 - 20. (New) An electroluminescence display device comprising a substrate provided with:

a display pixel region having an electroluminescence element including an emissive layer between an anode and a cathode, and first and second thin film transistors for driving said electroluminescence element; and

a drive circuit region having vertical and horizontal drive circuits that are located in a peripheral drive circuit region formed surrounding said display pixel region and are integrated on said substrate, said drive circuit region having third thin film transistors for driving said first and second thin film transistors,

wherein said cathode is disposed in said display pixel region and is absent from said drive circuit region,

said cathode comprises an opaque metal material and constitutes an uppermost layer of said electroluminescence element.

21. (New) The device as defined in Claim 20, wherein said cathode includes an end portion that extends to an area between said display pixel region and said drive circuit region.

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22. (New) An emissive display device comprising:

a distrilay pixel region disposed on a substrate and having an emissive element including an emissive layer between first and second electrodes; and

a drive circuit having vertical and horizontal drive circuits that are located in a peripheral drive circuit region formed surrounding said display pixel region and are integrated on said substrate, said drive circuit region having thin film transistors for driving said emissive element;

wherein said first electrode overlaps the entire display pixel region and is absent from at least said drive circuit region, said first electrode is a common cathode, and said second electrode is a discrete anode,

said cathode comprises an opaque metal material and constitutes an uppermost layer of said electroluminescence element.

- 23. (New) The device as defined in Claim 22, wherein said cathode includes an end portion that extends to an area between said display pixel region and said drive circuit region.
 - 24. (New) An electroluminescence display device comprising a substrate provided with:
- a display pixel region having an electroluminescence element including an emissive layer between an anode and a cathode, and first and second thin film transistors for driving said electroluminescence element, said cathode is formed in a layer extending above a layer in which said anode is formed; and

a drive circuit region having vertical and horizontal drive circuits that are located in a peripheral drive circuit region formed surrounding said display pixel region and are integrated on said substrate, said drive circuit region having third thin film transistors for driving said first and second thin film transistors,

wherein said cathode is disposed in said display pixel region and is absent from said drive circuit region, and

said cathode comprises an opaque metal material and constitutes an uppermost layer of said electroluminescence element.

25. (New) The device as defined in Claim 24, wherein said cathode includes an end portion that extends to an area between said display pixel region and said drive circuit region.

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26. (Now) An emissive display device comprising:

a display pixel region disposed on a substrate and having an emissive element including an emissive layer between first and second electrodes; and

a drive circuit region disposed on the same substrate surrounding said display pixel region, said drive circuit region having thin film transistors for driving said emissive element;

wherein said first electrode overlaps the entire display pixel region and is absent from at least said drive circuit region, said first electrode is a common cathode, and said second electrode is a discrete anode, and

said cathode includes an end portion that extends to an area between said display pixel region and said drive circuit region.